

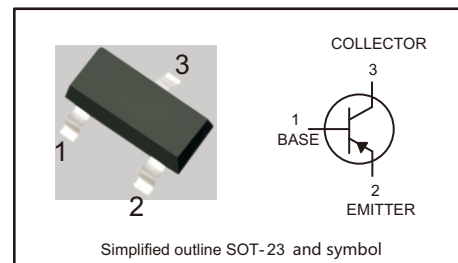
## PNP TRANSISTOR

### FEATURES

- High Collector-Emitter Voltage
- We declare that the material of product compliance with
- RoHS requirements and Halogen Free
- This device is designed for general-purpose amplifier
- Applications at collector currents to 500 mA.

### PINNING

PIN	DESCRIPTION
1	BASE
2	EMITTER
3	COLLECTOR



### MAXIMUM RATINGS (Ta =25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Collector– Base Voltage	VCBO	-60	V
Collector– Emitter Voltage	VCEO	-60	V
Emitter– Base Voltage	VEBO	-4	V
Collector Current — Continuous	IC	-0.5	A
Collector Power Dissipation	PC	0.225	W
Thermal Resistance From JunctionTo Ambient	RthJA	556	°C/W
Operation Junction and Storage Temperature Range	TJ , Tstg	-55~ +150	°C

### ELECTRICAL CHARACTERISTICS (TA = 25°C unless otherwise noted. )

Parameter	Symbol	Test conditions	Min	Typ	Max	Unit
Collector-base breakdown voltage	V(BR)CBO	IC = -100uA , IE = 0	-60			V
Collector-emitter breakdown voltage	V(BR)CEO	IC = -1mA , IB = 0	-60			V
Emitter-base breakdown voltage	V(BR)EBO	IE = -100uA , IC = 0	-4			V
Collector cut-off current	ICBO	VCB = -60V, IE = 0			-0.1	uA
Collector cut-off current	ICES	VCE = -60V, IB =0			-0.1	uA
DC current gain	hFE	VCE = -1V, IC = -10mA	100			
		VCE = -1V, IC = -100mA	100			
Collector-emitter saturation voltage	VCE(sat)	IC = -100mA , IB = -10mA			-0.25	V
Base-emitter voltage	VBE(ON)	VCE = -1V , IC = -0.1A			-1.2	V
Transition frequency	fT	VCE = -1V, IC = -100mA , f=100MHz	50			MHZ



### TYPICAL CHARACTERISTICS

Figure 1. Typical Pulsed Current Gain vs. Collector Current

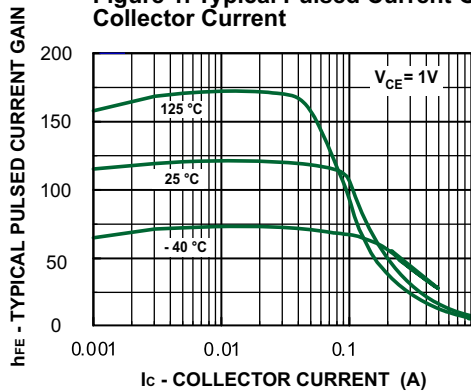


Figure 2. Collector-Emitter Saturation Voltage vs. Collector Current

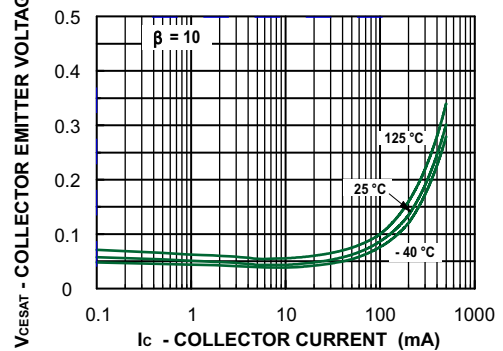


Figure 3. Base-Emitter Saturation Voltage vs. Collector Current

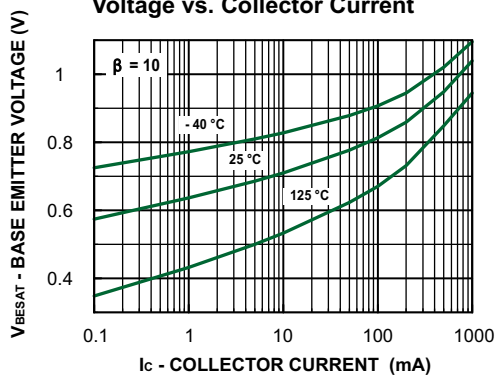


Figure 4. Base-Emitter On Voltage vs. Collector Current

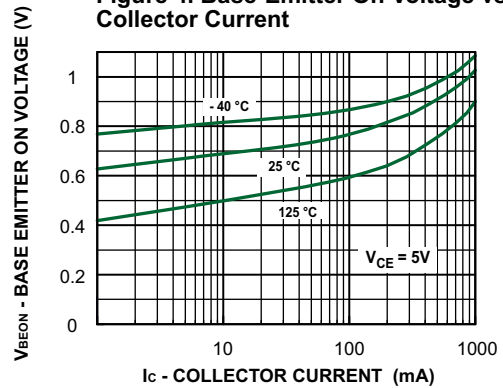
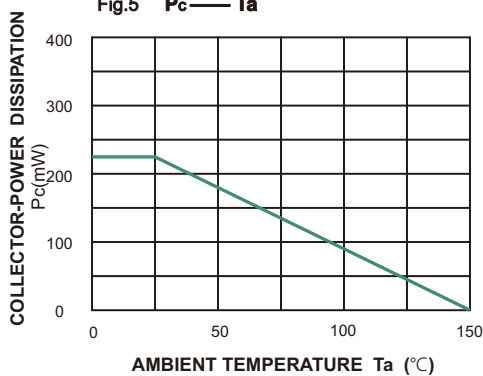
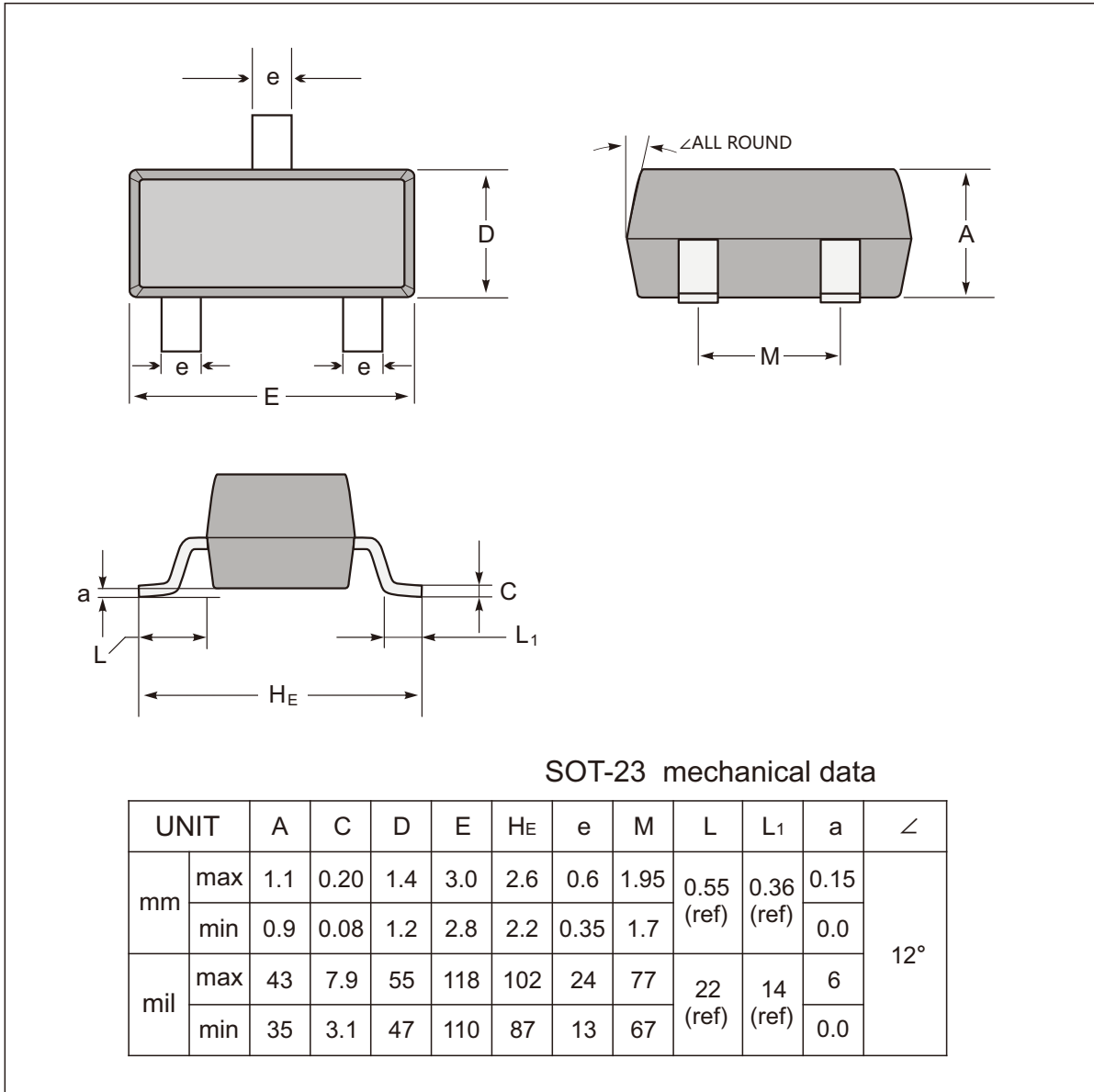


Fig.5  $P_c$  —  $T_a$

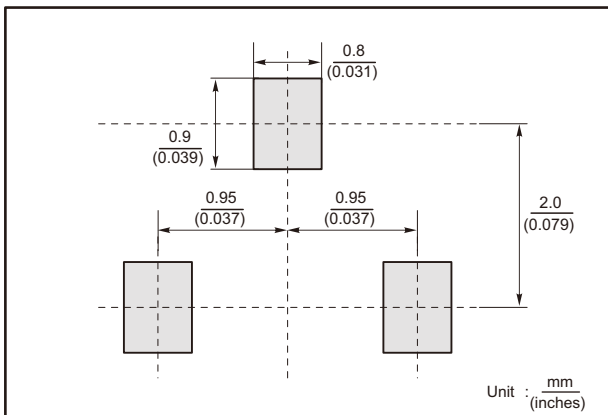




SOT-23 Package Outline Dimensions



The recommended mounting pad size



Marking

Type number	Marking code
MMBTA55WD	2H



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